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a phosphor formed over said second substrate;
a discharge gas sealed in the cavity between said first and second substrates; and
cells formed at intersections where said first and second electrodes cross said third
electrodes.

REMARKS

The foregoing new claims 15-21 correspond respectively to claims 43-49 heretofore pending in the immediate parent application Serial No. 09/013,538 and which were rejected therein and accordingly canceled, without prejudice, to enable early issuance of that immediate parent application as a U.S. letters patent.

No new matter is presented in the above new claims.

Applicants counsel respectfully requests a personal interview with the Examiner to discuss the claims now pending in the subject application and the patentability thereof over the art of record in the parent application. Applicants' counsel will be on a business trip from September 23, 2002 through October 10, 2002 and, accordingly, will contact the Examiner on or about October 22, 2002 to schedule the aforesaid interview.

If there are any additional fees associated with filing of this Amendment, please charge the same to our Deposit Account No. 19-3935.

Respectfully submitted,

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Date: September 20, 2002

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE CLAIMS:

Please ADD the following NEW claims:

15. (NEW) A flat plasma display, comprising:
an internal power supply circuit;
an internal power supply controlling unit producing power supply control signals
controlling an operation of said internal power supply circuit;
an external signal detection unit detecting a specific signal input to said plasma display
from an external source; and
a drive control signal control unit controlling drive control signals of said plasma display
in response to said detected specific signal.
16. (NEW) A flat plasma display as claimed in claim 15, wherein said internal power
supply controlling unit controls an operation of said internal power supply circuit by changing
said power supply control signals in response to said detected specific signal.
17. (NEW) A flat plasma display as claimed in claim 15, wherein said drive control
signal control unit controls an operation of a display panel driving unit by changing said drive
control signals in response to said detected specific signal.
18. (NEW) A flat plasma display as claimed in claim 15, wherein said control signal
control unit and said internal power supply controlling unit stop operating if said specific signal is
at a first level and start operating if said detected specific signal is at a second level, and
thereby the drive control signals are controlled in response to a level of said detected specific
signal.
19. (NEW) A flat plasma display comprising a three-electrode surface discharge AC
plasma display, further comprising:

an external signal detection unit detecting a specific signal input to said flat plasma display from an external source; and

a drive control signal control unit controlling drive control signals of said flat plasma display in response to said detected specific signal.

20. (NEW) A flat plasma display as claimed in claim 19, wherein said three-electrode surface discharge AC plasma display further comprises:

first and second electrodes arranged in parallel with each other; and

third electrodes orthogonal to said first and second electrodes, said first electrodes being commonly connected together and said second electrodes being arranged to define respective display lines, wherein said flat plasma display has a surface discharge structure employing wall charges as a memory.

21. (NEW) A flat plasma display as claimed in claim 20, wherein said three-electrode surface discharge AC plasma display further comprises:

a first substrate, said first and second electrodes being arranged in parallel to each other on said first substrate and paired for defining respective display lines;

a second substrate spaced apart from and facing said first substrate, defining a cavity therebetween, said third electrodes being arranged on said second substrate in orthogonal relationship to said first and second electrodes and displaced therefrom;

wall charge accumulating dielectric layers respectively covering the surfaces of said first and second electrodes;

a phosphor formed over said second substrate;

a discharge gas sealed in the cavity between said first and second substrates; and

cells formed at intersections where said first and second electrodes cross said third electrodes.